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Fig. 1

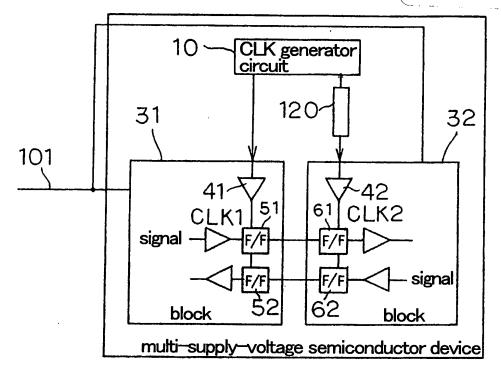
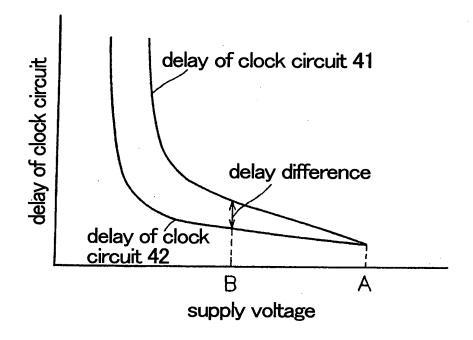


Fig. 2



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Fig. 3

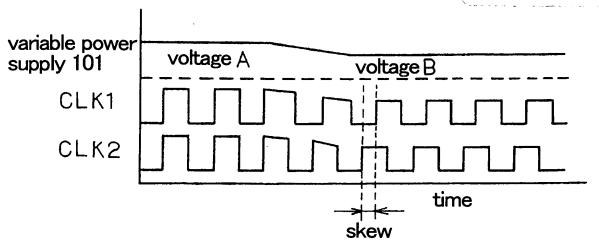
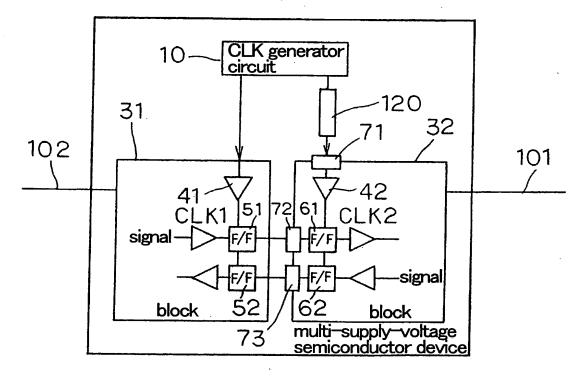


Fig. 4



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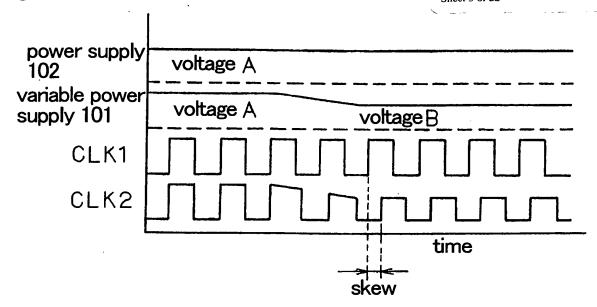


Fig. 6

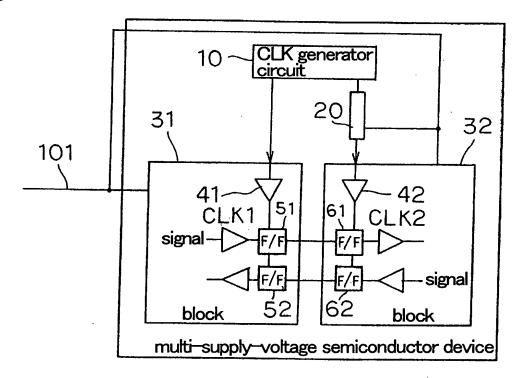


Fig. 7

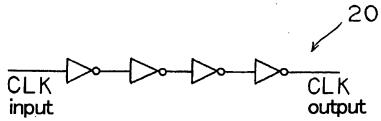


Fig. 8

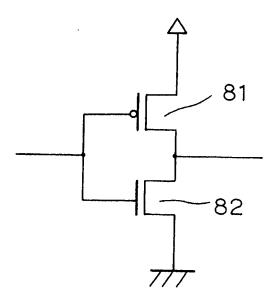
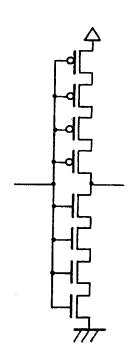


Fig. 9



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Fig. 10

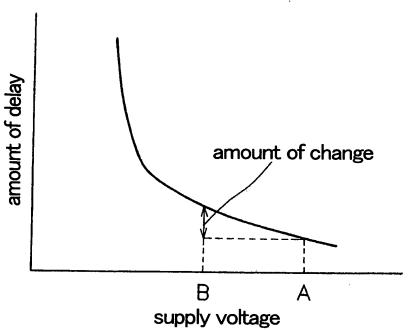
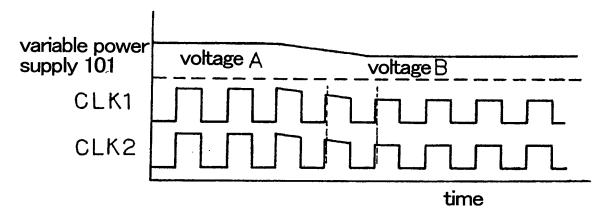


Fig. 11



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Fig. 12

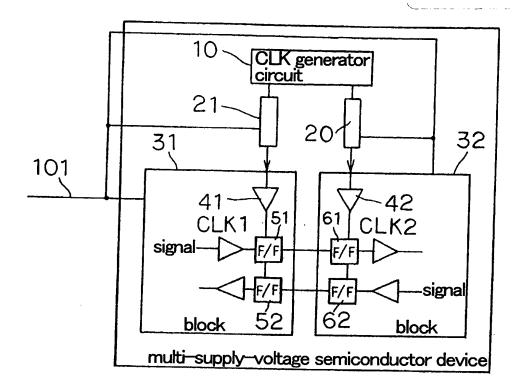
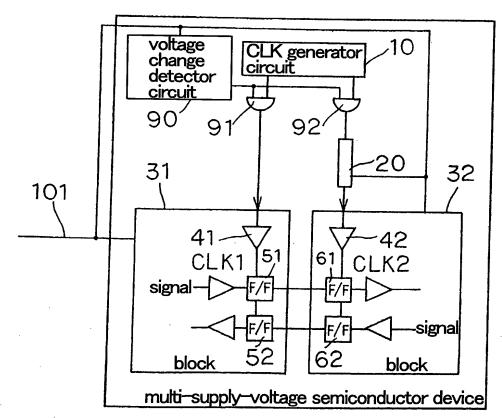


Fig. 13



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Fig. 14

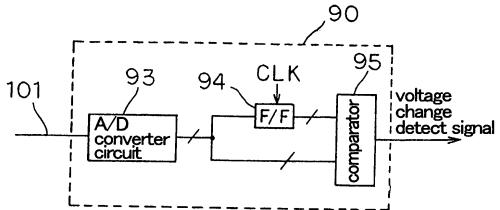
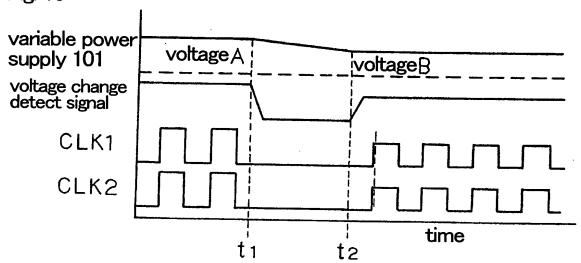


Fig. 15



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Fig. 16

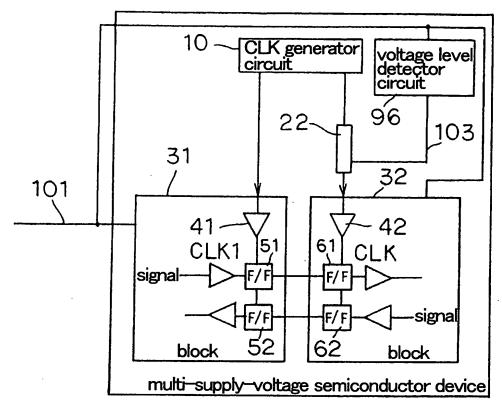


Fig. 17

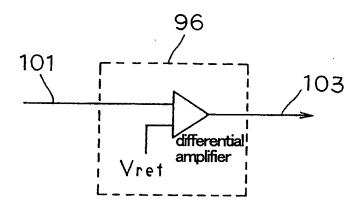
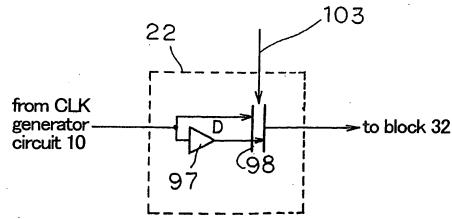


Fig. 18



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Fig. 19

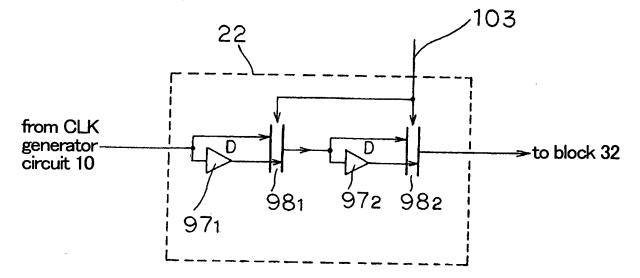


Fig. 20

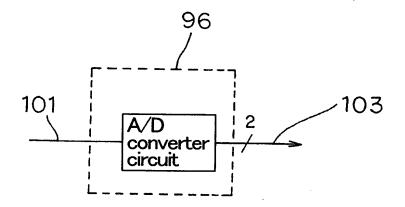


Fig. 21

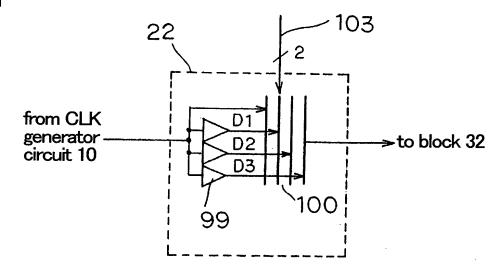


Fig. 22

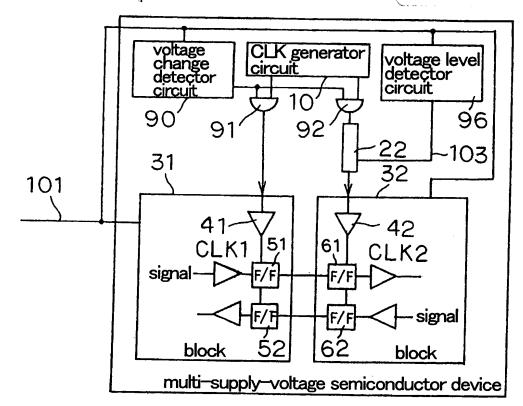


Fig. 23

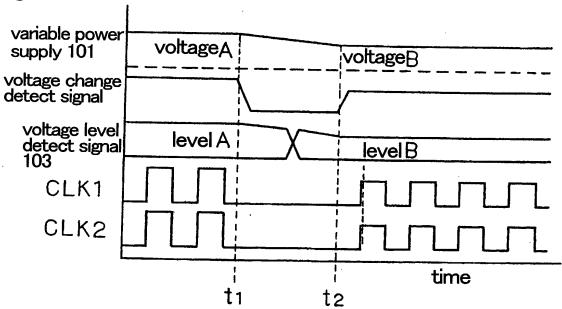


Fig. 24

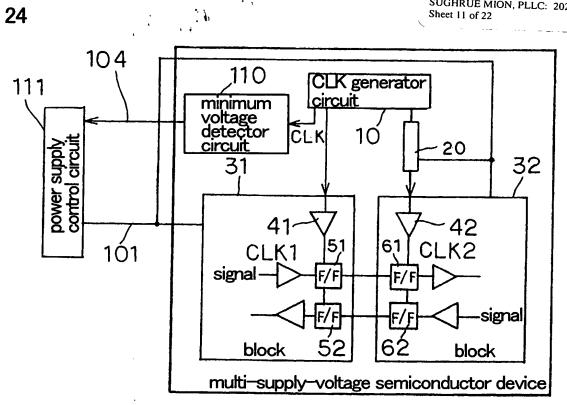
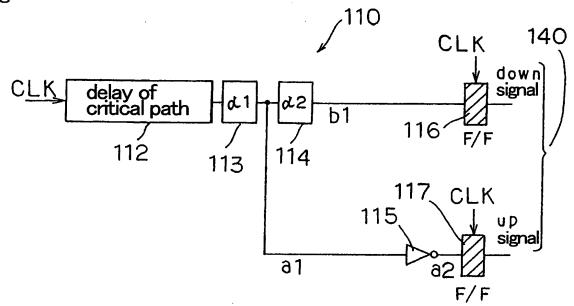


Fig. 25



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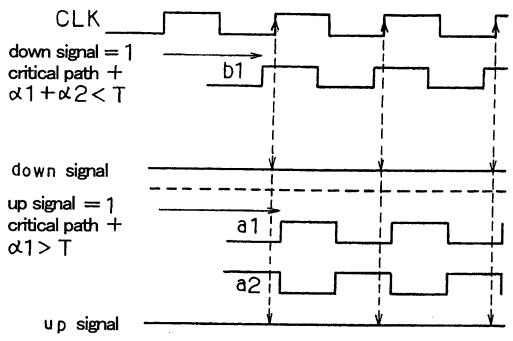


Fig. 27

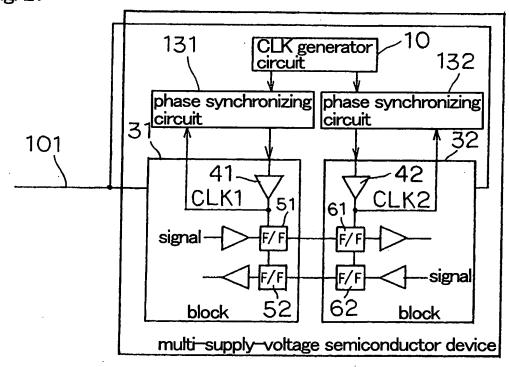
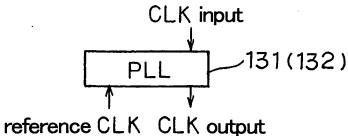


Fig. 28



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Fig. 29

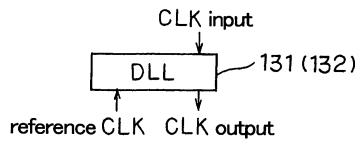


Fig. 30

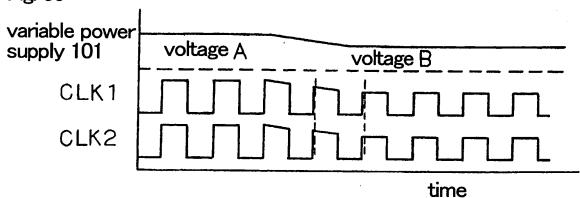


Fig. 31

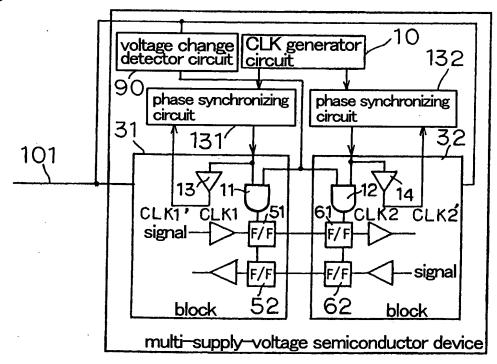
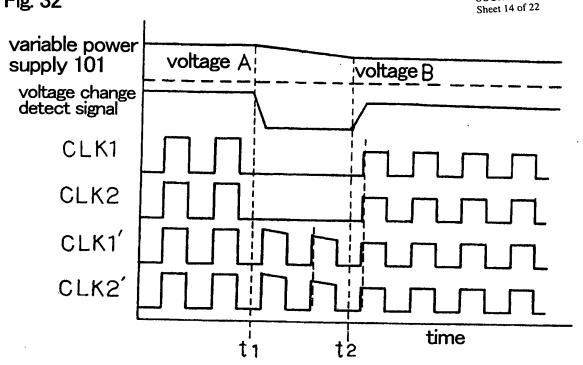
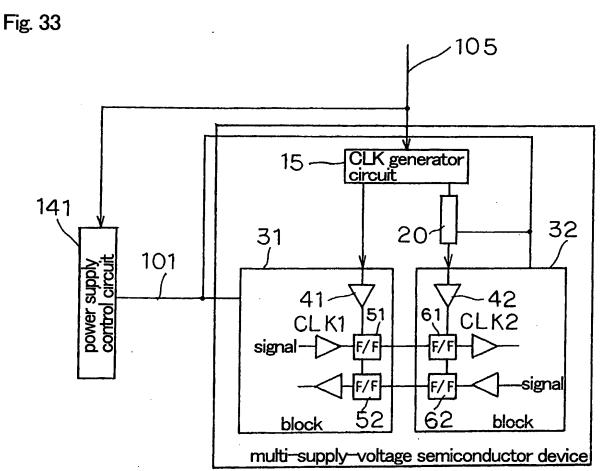


Fig. 32





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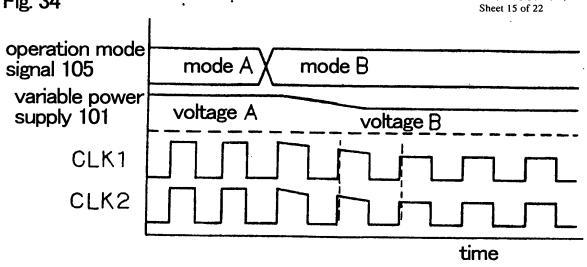
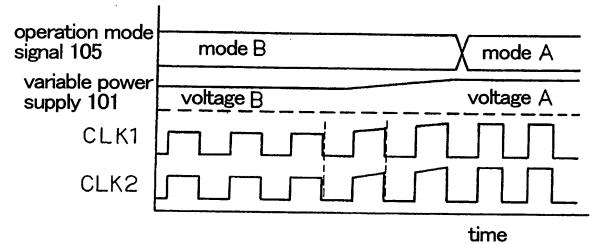
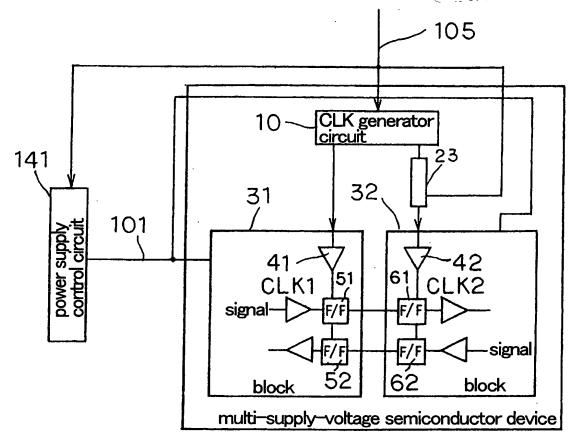


Fig. 35



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Fig. 36



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Fig. 37

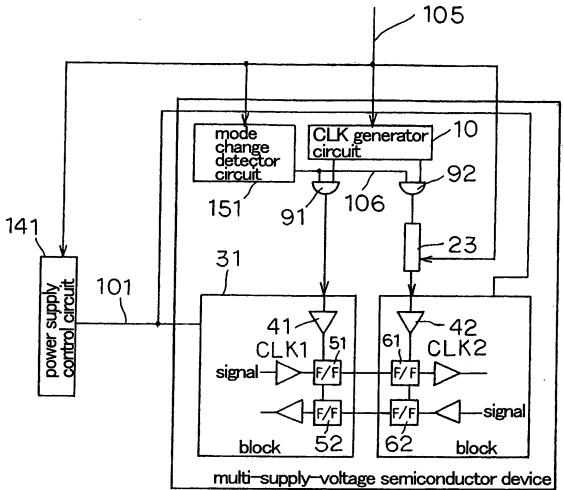
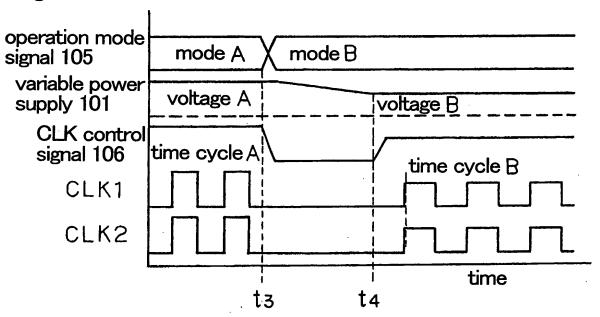


Fig. 38



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Fig. 39

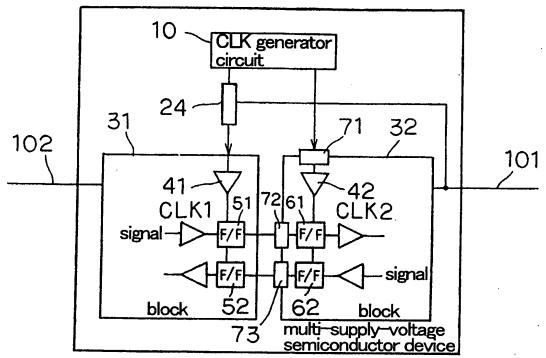


Fig. 40

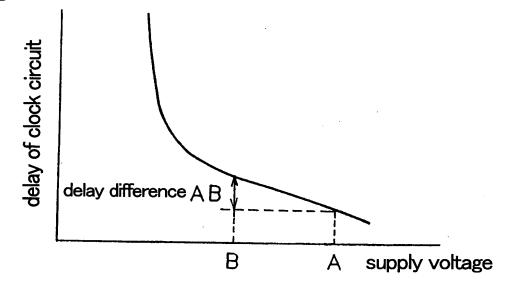
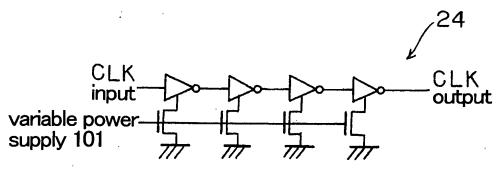


Fig. 41



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Fig. 42

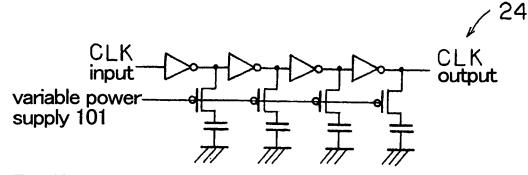


Fig. 43

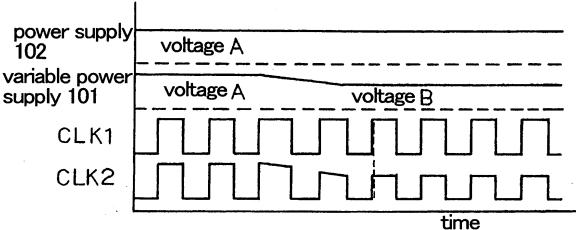


Fig. 44

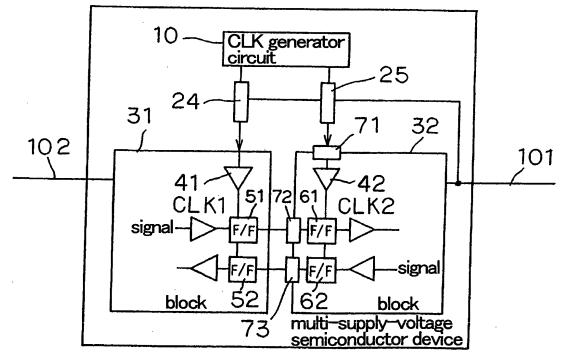


Fig. 45

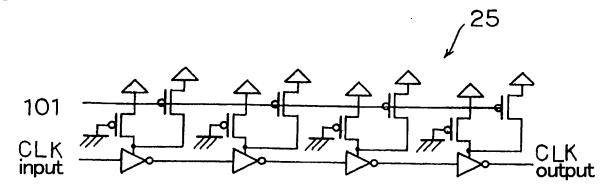


Fig. 46

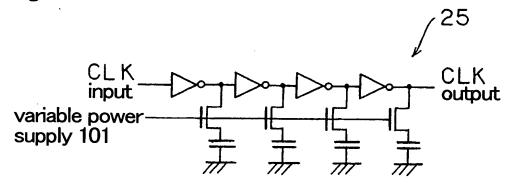


Fig. 47 CLK generator circuit 131 132 phase synchronizing phase synchronizing circuit circuit 32 26 102 ,101 41 72 61 51 signal signal block

multi-supply-voltage semiconductor device

Fig. 48

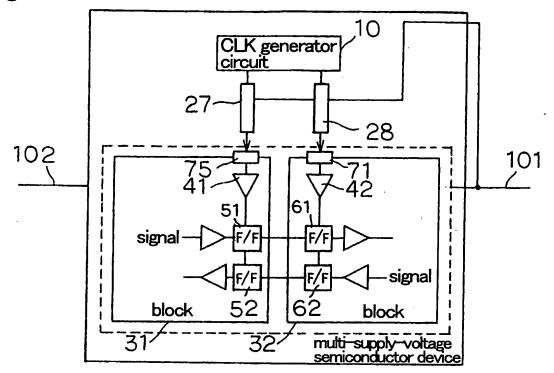
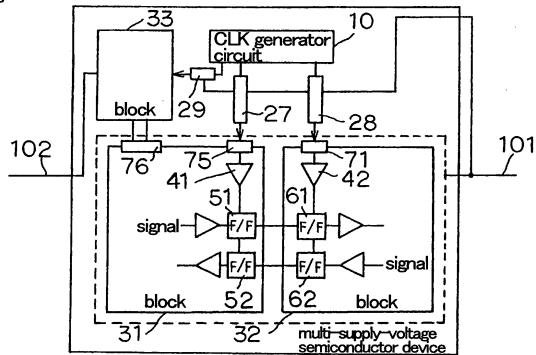


Fig. 49



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Fig. 50

